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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/707,110

11/21/2003

Kun-Hong Chen

12041-US-PA

1109

31561

7590

02/28/2005

JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE

7 FLOOR-1, NO. 100

ROOSEVELT ROAD, SECTION 2

TAIPEI, 100

TAIWAN

EXAMINER

A, MINH D

ART UNIT

PAPER NUMBER

2821

DATE MAILED: 02/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/707,110

Applicant(s)

CHEN, KUN-HONG

Examiner

Minh D. A

Art Unit

2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 5/12/04.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-12 is/are allowed.
- 6) ☒ Claim(s) 13-17 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 13-17 are rejected under 35 U.S.C. 102(e) as being unpatentable by Lin et al (US 2004/0188685 A1).

Regarding claim 13, Lin discloses a pixel structure of an active organic light emitting diode, comprising: an organic light emitting diode; a data-line; a scan-line (scan); a switch thin film transistor T1) having a first gate terminal, a first source terminal, a first drain terminal and a first lightly doped drain region, wherein the first gate terminal is coupled to the scan-line and the first drain terminal is coupled to the data-line; a control thin film transistor (T2) having a second gate terminal, a second source terminal and a second drain terminal, wherein the second gate terminal is coupled to

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the first source terminal and the second drain terminal is coupled to the organic light emitting diode; and a capacitor (Cs) coupled to the first drain terminal and to the second gate terminal. See figures 2-3, col.3, lines [0043] to col.4, lines [0053] to lines [0058].

Regarding claims 14-17, Lin inherently discloses wherein the switch thin film transistor is a P-type low-temperature poly-silicon thin film transistor and wherein the control thin film transistor is a P-type low-temperature poly-silicon thin film transistor or wherein the switch thin film transistor is an N-type low temperature poly-silicon thin film transistor and wherein the control thin film transistor is an N-type low-temperature poly-silicon thin film transistor, since Lin discloses that, the transistors can be any type of thin film transistor as show on figure 1b, col.3, lines [0036] to lines [0040].

Allowable Subject Matter

3. Claims 1-12 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art does not teach that, a control thin film transistor having a second gate terminal, a second source terminal, a second drain terminal and a second lightly doped drain region, wherein the second drain terminal is coupled to the organic light emitting diode and the first lightly doped drain region and the second lightly doped drain region have different doped concentrations; and a capacitor coupled to the first drain terminal and to the second gate terminal in combination with all limitations recited in independent claim 1.

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The prior art does not teach that, a control thin film transistor having a second gate terminal, a second source terminal, a second drain terminal and a second lightly doped drain region, wherein the second gate terminal is coupled to the first source terminal, the second drain terminal is coupled to the organic light emitting diode and the first lightly doped drain region and the second lightly doped drain region have different lengths; and a capacitor coupled to the first drain terminal and to the second gate terminal with all limitations recited in independent claim 7.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yamazaki et al (US 6,747,638) and Anza. (US 6,798,405) are cited to show a thin film transistor.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Minh A whose telephone number is (571) 272-1817. The examiner can normally be reached on M-F (5:30 –2:30 PM).

If attempts to reach the examiner by telephone is unsuccessful, the examiner's supervisor, Don Wong, can be reached on (571) 272-1834. The fax phone numbers for

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the organization where this application or proceeding is assigned are 703-872-9306 for regular communications and (703) 872-9319 for final communications.

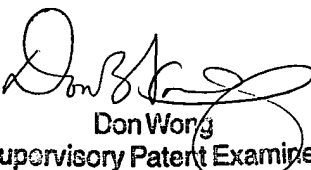
Any inquiry of a general nature or relating to the status of this application should be directed to the Technology Center receptionist whose telephone number is (571) 272-1553.

Examiner

Minh A

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2/20/05


Don Wong
Supervisory Patent Examiner
Technology Center 2600